

Al-Mustaqbal University College		
College of Engineering and Technology		
Biomedical Engineering Department		
Stage:2 th Sub: Digital Logic Design		
Week	Laboratory Work	
1 st	Introduction of digital system, logic operations	
2 nd	Number System ,operations and codes	
3 rd	Logic gates	
4 th	Boolean Algebra	
5 th	Logic Simplification	
6 th	Combinational logic Analysis	
7 th	Functions of combinational logic(Adder, Comparator)	
8 th	Decoders, encoders, multiplexer and demultiplexer	
9 th	Latches	
10 th	Flip –Flops	
11 th	Applications Flip –Flops	

12th	Shift registers
13th	Asynchronous Counter
14th	Synchronous counter
15th	Memory and storage