Al-Mustaqbal University College	
College of Engineering and Technology	
Biomedical Engineering Department	
Stage:3th Sub: Electronics Laboratory	

Week	Laboratory Work
1st	Introduction to Digital Electronics lab- nomenclature of digital
2nd	Implementation of the given Boolean function using logic gates
3rd	Both sop and pos forms.
4th	Verification of state tables of RS, JK, T and D flip-flops using NAND & nor gates
5th	Implementation of 4x1 multiplexer using logic gates
6th	Implementation of 4-bit parallel adder using 7483 IC
7th	First Examination
8th	Design and verify the 4-bit synchronous counter
9th	Design and verify the 4-bit synchronous counter asynchronous counter
10th	To design and verify operation of half adder and full adder. 28-2
11th	Examination

12^{th}	**Design and verify operation of half subtractor**
13^{th}	**Design & verify the operation of magnitude comparator.**
14^{th}	**Study and verify NAND as a universal gate**
15^{th}	**Exam**