Experiment No.2

# Half Wave Rectifier

**Objectives:**

* **To calculate and draw the DC output voltages of half- wave rectifiers.**
* **Without smoothing capacitor and with smoothing capacitor.**

# Apparatus:

1. AC power supply or Function Generator.
2. Function Generator Oscilloscope.
3. Diode: Silicon D1N4007 .
4. Resistors: 10 kΩ, Capacitor 1F

# Theory:

***Half Wave Rectifier:***

A diode is a unidirectional conduction device. It conducts only when its anode is at a higher voltage with respect to its cathode. In a halfwave rectifier circuit, during positive half-cycle of the input, the diode gets forward biased and it conducts. Current flows through the load resistor R L and voltage is developed across it. During negative half-cycle of the input, the diode gets

reverse biased. Now no current (except the leakage current which is very small) flow. The voltage across the load resistance during this period of input cycle is zero. Thus, a pure ac signal is converted into a unidirectional signal. It can be shown that (i) V dc = V m/𝜋 Where, V dc is the output DC voltage and Vm is peak AC voltage at the input the rectifier circuit.

# Procedure:

1. Connect the circuit as shown in Figure (1) using silicon diode. , adjust the power supply at 10 V.
2. Measure the input voltage V1 and the output voltage V2 using both the voltmeter and the oscilloscope.
3. Draw the input waveform, Vi, and the output waveform, Vo
4. Calculate:
	1. Maximum voltage of the input signal

**Vm**= 𝐕p-p/√2

* 1. the effective value of the input voltage V= 𝐕𝐦/√2
	2. the average value of the output voltage

**Vav**  **Vdc**  **0.318Vm**

1. Connect the circuit as shown in Fig.2.
2. Draw the output waveform with Smoothing capacitor C= 1F .

# Discussion:

1. What is rectifier and what is the Ripple Factor?
2. what the effect of increasing or decreasing CL& R L?
3. what is the Efficiency find it for all result?





