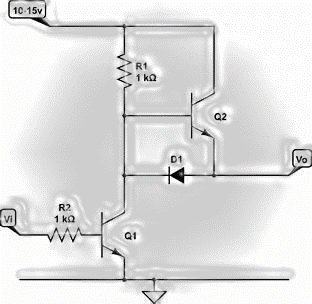
**Electronic Circuit**

**Lecture 6 ( Week)**

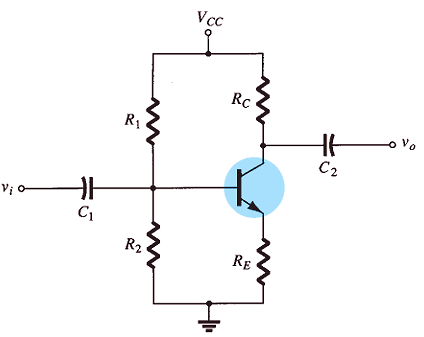
**Multiple BJT Networks**



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| Lecture Description | |
| **Target Audience** | The second stage for undergraduate students / Department of Medical Instrumentation Techniques Engineering |
| **General Objective** | studying **Multiple BJT Networks** is to understand how to design and analyze circuits that use more than one Bipolar Junction Transistor (BJT) in various configurations. |
| **Central Idea** | Multiple BJT Networks is to use Bipolar Junction Transistors (BJTs) in various interconnected configurations to create complex electronic circuits that provide specific characteristics, such as amplification, current control, and signal integration. |
| **Behavioral Objectives** | Upon completing this lecture, students will be able to:   * The different types of bipolar junction transistor (BJT) networks used in advanced circuits. * States the fundamental laws that govern the operation of these networks, such as Ohm's Law and Kirchhoff's laws of current and voltage. * Explains how transistors work in multi-layer networks, and how the characteristics of each layer affect the others. * Is able to analyze a circuit containing multiple BJT networks using analysis techniques. |
| **Time** | 2 hr. |
| **Academic Year** | 2024-2025 |

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| وصف المحاضرة | |
| طلبة الدراسة الاولية من المرحلة الثانية /قسم هندسة تقنيات الأجهزة الطبية | **الفئة المستهدفة** |
| دراسة **شبكات الترانزستورات الثنائية القطب المتعددة هو** فهم كيفية تصميم وتحليل الدوائر التي تستخدم أكثر من ترانزستور واحد من نوع BJT في تكوينات متنوعة | **الهدف العام** |
| استخدام ترانزستورات الوصلة الثنائية (BJT) في تكوينات متعددة ومتداخلة لإنشاء دوائر إلكترونية معقدة توفر خصائص معينة، مثل التضخيم، والتحكم في التيار، وتكامل الإشارة | **الفكرة الرئيسية** |
| عند الانتهاء من هذه المحاضرة، سيتمكن الطالب من:   * أن الأنواع المختلفة من شبكات الترانزستور الثنائية القطب (BJT) المستخدمة في الدوائر المتقدمة. * يذكر القوانين الأساسية التي تحكم عمل هذه الشبكات، مثل قانون أوم وقانون كيرشوف للتيار والجهد * يشرح كيفية عمل الترانزستورات في شبكات متعددة الطبقات، وكيف تؤثر خصائص كل طبقة على الأخرى * يتمكن من تحليل دائرة تحتوي على شبكات متعددة من الترانزستورات الثنائية القطب باستخدام التحليل. | **الأهداف السلوكية** |
| 2 ساعة | **الوقت** |
| 2024-2025 | **السنة الدراسية** |

**4.5. Voltage Divider Bias Configuration**

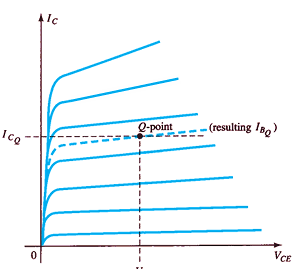


**Fig. (1). Voltage-divider bias configuration.**

When analyzed precisely, the sensitivity of the circuit to variations in beta is relatively low. If the circuit parameters are chosen appropriately, the resulting levels of and ​ can be nearly independent of beta. Recall from earlier discussions that the Q-point is defined by specific levels of and ​, as illustrated in Fig. (2). While the level of ​ will change with variations in beta, the operating point determined by ​ and can remain stable if the circuit parameters are correctly selected.

**Methods for Analyzing the Voltage-Divider Configuration:**

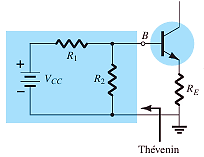
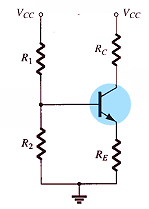
* There are two methods for analyzing the voltage-divider configuration:
  + **Exact Method**: This method can be applied to any voltage-divider configuration and provides precise results.
  + **Approximate Method**: This method can only be applied under specific conditions and allows for a quicker and simpler analysis.



**Fig. (2). Defining the Q-point for the voltage-divider bias configuration.**

**Exact Method**

For the DC analysis, the network an be modified as depicted in Fig. (3). Subsequently, the input side of the network can be simplified further for DC analysis. Using this configuration, the Thevenin equivalent network for the portion of the circuit left of the base terminal can be determined as follows:



**Fig. (3). DC components of the voltage divider configuration and redrawing the input side of the network.**

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The voltage source is substituted with a short-circuit equivalent,

The voltage source ​ is reintroduced to the network, and the open-circuit Thevenin voltage of Fig. 4. is determined as follows: using the voltage-divider rule, we get:

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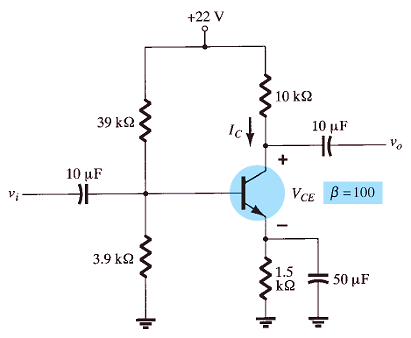
**Fig. (4).** **Inserting the Thevenin equivalent circuit**

The Thevenin network is then redrawn. by first applying Kirchhoff’s voltage law in the clockwise direction for the loop indicated:

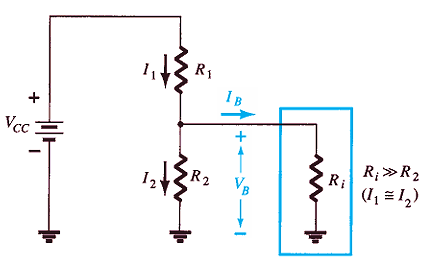
Substituting and solving for yields:

the remaining quantities of the network can be found in the same manner as developed for the emitter-bias configuration. That is:

**Example 1:** **Determine the dc bias voltage V CE and the current I C for the voltage divider configuration of Fig. (5).**



**Approximate Method**



**Fig. (5). Partial-bias circuit for calculating the approximate base voltage**

the key points for the input section of the voltage-divider configuration:

* The input section of the voltage-divider configuration can be modeled by a network similar to Fig. (5).
* Represents the equivalent resistance between the base and ground for the transistor, with an emitter resistor ​.
* The reflected resistance between the base and emitter is given by:
* When is significantly larger than the resistance :
  + The base current ​ will be much smaller than .
  + Current ​ will be approximately equal to ​ because current follows the path of least resistance.
* Assuming ​ is nearly 0 A compared to ​ and ​:
  + Resistors and ​ can be treated as series elements.
* The voltage across ​ (also the base voltage) can be calculated using this series relationship.

It is determined using the voltage-divider rule, which is where the configuration gets its name.

Since ​, the condition that determines whether the approximate approach can be applied is: ​.

​

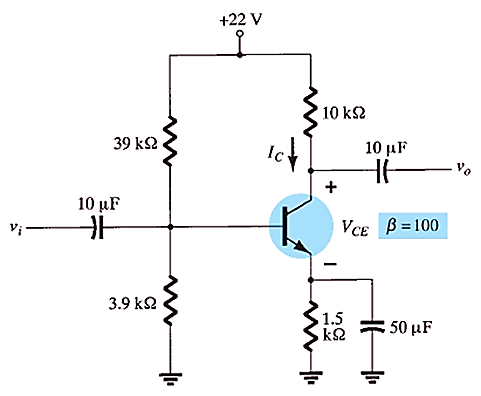
Once ​ is determined, the value of ​ can then be calculated:

and the emitter current can be determined from

And,

The collector-to-emitter voltage is determined by:

**Example 2:** Repeat the analysis of Fig. below using the approximate technique, and compare solutions for and .



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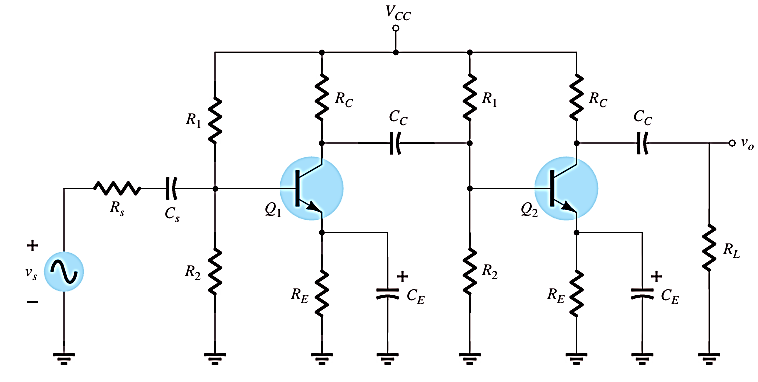
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The main difference between the exact and approximate techniques lies in the effect of in the exact analysis, which separates ​ and

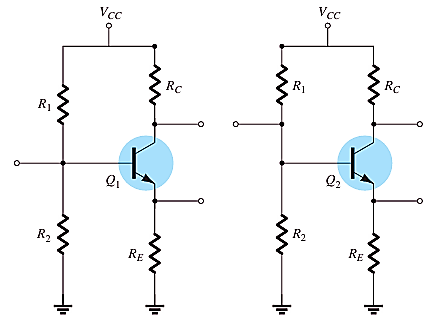
Compared to 0.84 mA with the exact analysis. Finally,

**4.6. Multiple BJT Networks**

So far, only single-stage BJT configurations have been introduced. This section will cover some popular networks using multiple transistors, demonstrating how the methods introduced in this chapter can be applied to networks with any number of components. The R-C coupling in Fig. (6). is one of the most common, where the collector output of one stage is directly connected to the base of the next stage using a coupling capacitor ​. The capacitor is selected to block DC between stages while acting as a short circuit for any AC signal. This network has two voltage-divider stages, but the same coupling can be applied between any configurations, such as fixed-bias or emitter-follower setups. Replacing and other capacitors with open-circuit equivalents yields the two bias arrangements shown in Fig. (7), allowing each stage to be analyzed separately without interference from the other. Of course, the 20 V DC supply must be applied to each isolated component.



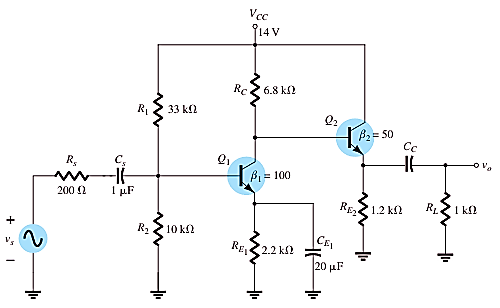
**Fig. (6). R–C coupled BJT amplifiers**



**Fig. (7). DC equivalent of Fig. (6).**

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| For the dc analysis of Fig. (8). assuming a beta for the first transistor and for the second:  Assuming for each transistor, we find the net beta for the configuration is: |  |
| **Fig.(8). DC equivalent Circuit** |

**Example 3:** **Determine the dc levels for the currents and voltages of the direct- coupled amplifier of Fig. (9).**



**Fig. (9). Direct- coupled amplifier.**

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**Summary:**

* Relies on dividing voltage using resistors.
* Resistors are connected in series to divide voltage between components.
* Used to set and adjust the appropriate voltage in electronic circuits.
* Commonly used in signal control and amplification circuits.
* Involves circuits with more than one BJT transistor working together.
* Used to enhance functional performance such as amplification or signal control.
* Can be in series, parallel, or multi-phase configurations.
* Improves frequency response and signal amplification efficiency.
* Increases control over current flow and isolates inputs from outputs.