



Al-Mustaqbal University
**College of Engineering and
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Stage: Second

Electric circuit II

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Lecture (9): parallel AC



and the equivalent admittance is

$$Y_{eq} = Y_1 + Y_2 + \dots + Y_N \quad (9.47)$$

This indicates that the equivalent admittance of a parallel connection of admittances is the sum of the individual admittances.

When $N = 2$, as shown in Fig. 9.20, the currents in the impedances are

$$I_1 = \frac{Z_2}{Z_1 + Z_2} I, \quad I_2 = \frac{Z_1}{Z_1 + Z_2} I \quad (9.48)$$

which is the current-division principle.

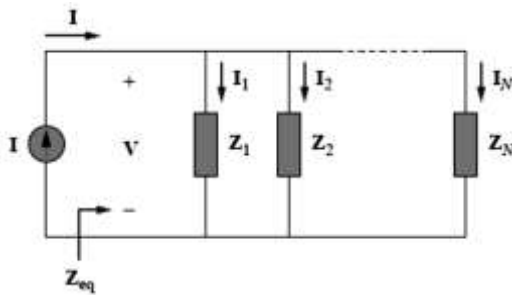


Figure 9.19 N impedances in parallel.

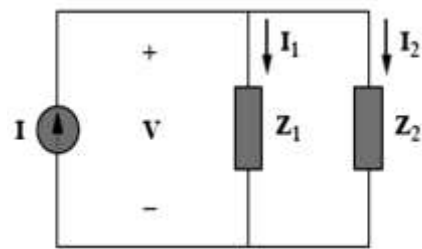


Figure 9.20 Current division.

The delta-to-wye and wye-to-delta transformations that we applied to resistive circuits are also valid for impedances. With reference to Fig. 9.21, the conversion formulas are as follows.

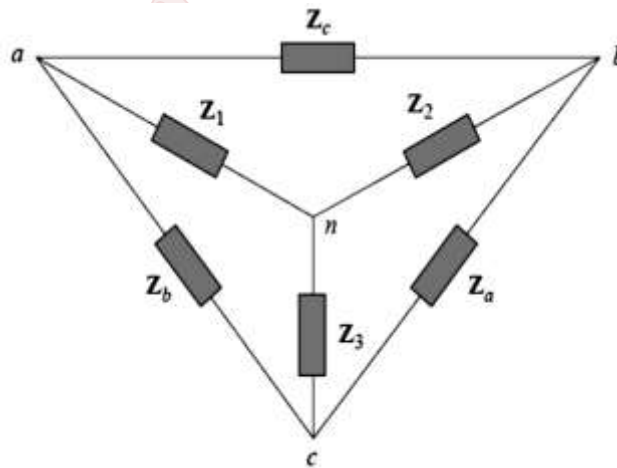


Figure 9.21 Superimposed Y and Δ networks.

Y - Δ Conversion:

$$Z_a = \frac{Z_1 Z_2 + Z_2 Z_3 + Z_3 Z_1}{Z_1}$$

$$Z_b = \frac{Z_1 Z_2 + Z_2 Z_3 + Z_3 Z_1}{Z_2}$$

$$Z_c = \frac{Z_1 Z_2 + Z_2 Z_3 + Z_3 Z_1}{Z_3}$$

(9.49)



Δ -Y Conversion:

$$Z1 = \frac{Zb Zc}{Za + Zb + Zc}$$

$$Z2 = \frac{Zc Za}{Za + Zb + Zc}$$

$$Z3 = \frac{Za Zb}{Za + Zb + Zc}$$

(9.50)

A delta or wye circuit is said to be balanced if it has equal impedances in all three branches.

When a Δ -Y circuit is balanced, Eqs. (9.49) and (9.50) become

$$Z\Delta = 3ZY \quad \text{or} \quad ZY = \frac{1}{3} Z\Delta$$

(9.51)

where $ZY = Z1 = Z2 = Z3$ and $Z\Delta = Za = Zb = Zc$.

Example 9.5: Find the input impedance of the circuit in Fig. 9.23. Assume that the circuit operates at $\omega = 50$ rad/s.

Solution:

Let

$Z1$ = Impedance of the 2-mF capacitor

$Z2$ = Impedance of the 3- Ω resistor in series with the 10-mF capacitor

$Z3$ = Impedance of the 0.2-H inductor in series with the 8- Ω resistor

Then

$$Z1 = \frac{1}{j\omega C} = \frac{1}{j 50 \times 2 \times 10^{-3}} = -j10 \, \Omega$$

$$Z2 = 3 + \frac{1}{j\omega C} = 3 + \frac{1}{j 50 \times 10 \times 10^{-3}} = (3 - j2) \, \Omega$$

$$Z3 = 8 + j\omega L = 8 + j50 \times 0.2 = (8 + j10) \, \Omega$$

The input impedance is

$$\begin{aligned} Z_{in} &= Z1 + Z2 || Z3 = -j10 + \frac{(3 - j2)(8 + j10)}{11 + j8} \\ &= -j10 + \frac{(44 + j14)(11 - j8)}{11^2 + 8^2} = -j10 + 3.22 - j1.07 \, \Omega \end{aligned}$$

Thus,

$$Z_{in} = 3.22 - j11.07 \, \Omega$$

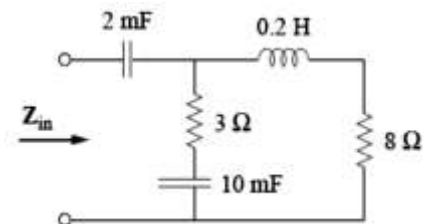


Figure 9.22



Practice problem 9.5: Determine the input impedance of the circuit in Fig. 9.24 at $\omega = 10$ rad/s.

Answer: $32.38 - j73.76 \Omega$.

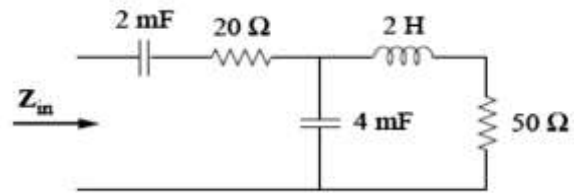


Fig. 9.24

Example 9.6: Determine $v_o(t)$ in the circuit in Fig. 9.25.

Solution: To do the analysis in the frequency domain, we must first transform the time domain circuit in Fig. 9.25 to the phasor-domain equivalent in Fig. 9.26. The transformation produces

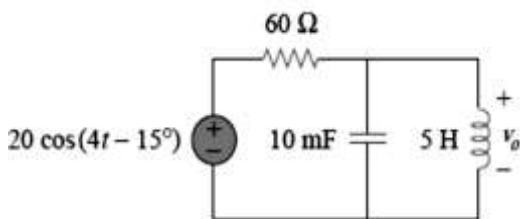


Figure 9.25 For Example 9.11.

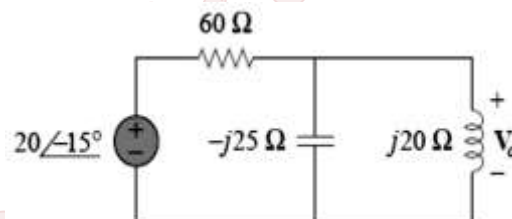


Figure 9.26 The frequency-domain equivalent of the circuit in Fig. 9.25.

$$v_s = 20 \cos(4t - 15^\circ) \Rightarrow V_s = 20 \angle -15^\circ \text{ V}, \omega = 4$$

$$10 \text{ mF} \Rightarrow \frac{1}{j\omega C} = \frac{1}{j4 \times 10 \times 10^{-3}} = -j25 \Omega$$

$$5 \text{ H} \Rightarrow j\omega L = j4 \times 5 = j20 \Omega$$

Let

Z_1 = Impedance of the 60- Ω resistor

Z_2 = Impedance of the parallel combination of the 10-mF capacitor and the 5-H inductor

Then $Z_1 = 60 \Omega$ and

$$Z_2 = -j25 \parallel j20 = \frac{-j25 \times j20}{-j25 + j20} = j100 \Omega$$

By the voltage-division principle,

$$\begin{aligned} V_o &= \frac{Z_2}{Z_1 + Z_2} V_s = \frac{j100}{60 + j100} (20 \angle -15^\circ) \\ &= (0.8575 \angle 30.96^\circ) (20 \angle -15^\circ) = 17.15 \angle 15.96^\circ \text{ V.} \end{aligned}$$

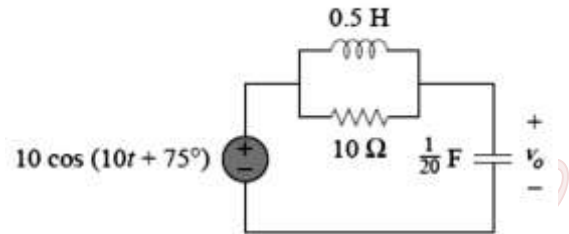
We convert this to the time domain and obtain



$$v_o(t) = 17.15 \cos(4t + 15.96^\circ) \text{ V}$$

Practice problem 9.6: Calculate v_o in the circuit in Figure below.

Answer: $v_o(t) = 7.071 \cos(10t - 60^\circ) \text{ V}$.



Example 9.7: Find current I in the circuit in Fig. 9.27.

Solution:

The delta network connected to nodes a , b , and c can be converted to the Y network of Fig. 9.28. We obtain the Y impedances as follows using Eq. (9.49):

$$Z_{an} = \frac{j4(2 - j4)}{j4 + 2 - j4 + 8} = \frac{4(4 + j2)}{10} = (1.6 + j0.8) \Omega$$

$$Z_{bn} = \frac{j4(8)}{10} = j3.2 \Omega, \quad Z_{cn} = \frac{8(2 - j4)}{10} = (1.6 - j3.2) \Omega$$

The total impedance at the source terminals is

$$\begin{aligned} Z &= 12 + Z_{an} + (Z_{bn} - j3) \parallel (Z_{cn} + j6 + 8) \\ &= 12 + 1.6 + j0.8 + (j0.2) \parallel (9.6 + j2.8) \\ &= 13.6 + j0.8 + \frac{j0.2(9.6 + j2.8)}{9.6 + j3} \\ &= 13.6 + j1 = 13.64 \angle 4.204^\circ \end{aligned}$$

The desired current is

$$I = \frac{V}{Z} = \frac{50 \angle 0^\circ}{13.64 \angle 4.204^\circ} = 3.666 \angle -4.204^\circ \text{ A}$$

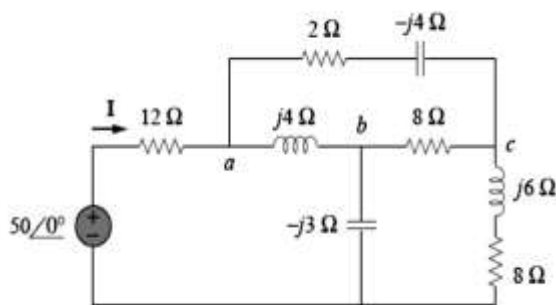


Figure 9.27

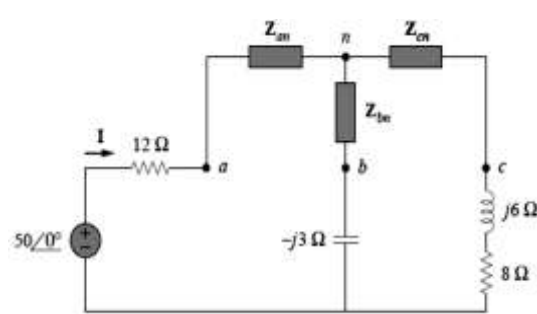


Figure 9.28 The circuit in Fig. 9.27 after delta-to-wye transformation.

Practice problem 9.8: Find I in the circuit in Figure below.



Answer: $6.364 \angle 3.802^\circ \text{ A}$.

