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اسم الماده: تقنيات رقمية اسم المحاضر: مدرس جابر حميد مجيد

For Second class

by Lec. Jaber Hamed

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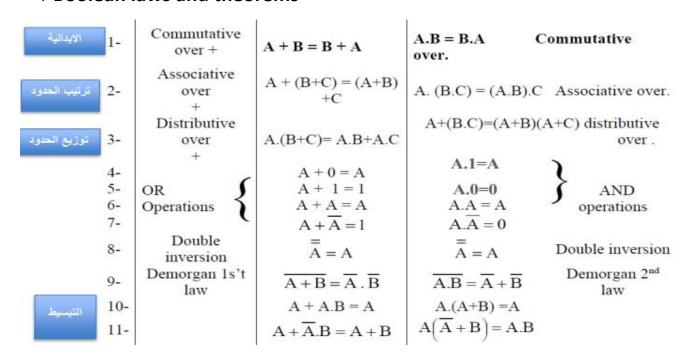
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Boolean Algebra

Because binary logic is used in all of today's digital computers and devices, the cost of the circuits that implement it is an important factor addressed by designers—be they computer engineers, electrical engineers, or computer scientists. Finding simpler and cheaper, but equivalent, realizations of a circuit can reap huge payoffs in reducing the overall cost of the design. Boolean algebra that will enable you to optimize simple circuits and to understand the purpose of algorithms used by software tools to optimize complex circuits involving millions of logic gates. Boolean algebra, like any other deductive mathematical system, may be defined with a set of elements, a set of operators, and a number of unproved axioms or postulates.

Boolean algebra is an algebraic structure defined on a set of elements B together with two binary operators (+) and (.) The two valued Boolean algebra is defined on a set of two elements. B ={0,1}.

Boolean laws and theorems



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❖ Duality:

This important property of Boolean algebra is called the duality principle and states that every algebraic expression deducible from the postulates of Boolean algebra remains valid if the operators and identity elements are interchanged.

- 1- Changing every OR by AND and vice versa.
- 2- Changing every 1 to 0 and every 0 to 1.

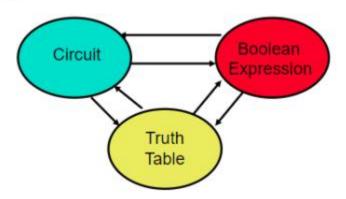
Example:
$$A + 0 = A$$
 duality the 0 $A \cdot 1 = A$
 $A(B + C) = AB + AC \rightarrow A + BC = (A + B)(A + C)$

The Proof of theorems 10 & 11

Theorem 10
$$\rightarrow$$
 (a) A + AB = A.1 + AB = A(1+B) = A
(b)A.(A + B) = A.A + AB = A + AB = A.1 + AB
= A(1+B) = A

Theorem 11 (a)A +
$$\overline{AB}$$
 = (A + \overline{A}).(A + B) = 1.(A + B) = A + B
(b)A.(\overline{A} + B) = A. \overline{A} + A.B = 0 + A.B = A.B

or it can proved using duality theorem



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* truth table of each gate

| INPUTS | | OUTPUTS | | | | | |
|--------|---|---------|------|----|-----|------|-------|
| Α | В | AND | NAND | OR | NOR | EXOR | EXNOR |
| 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 |
| 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 |
| 1 | 0 | 0 | 1 | 1 | 0 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | 0 | 0 | 1 |

Truth Tables for the Laws of Boolean

| Boolean Expression | Description | Equivalent Switching Circuit | Boolean Algebra Law or Rule |
|-----------------------|--|---------------------------------|--------------------------------|
| A+1=1 | A in parallel with closed = "CLOSED" | A | Annulment |
| A+0=A | A in parallel with open = "A" | A | Identity |
| A.1=A | A in series with closed = "A" | | Identity |
| A.0=0 | A in series with open = "OPEN" | | Annulment |
| A + A = A | A in parallel with A = "A" | A | Idempotent |
| A.A=A | A in series with A = "A" | A A . | Idempotent |
| NOT A = A | NOT NOT A (double negative) = "A" | | Double Negation |
| A + A = 1 | A in parallel with NOT A = "CLOSED" | Ā | Complement |

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| A.Ā=0 | A in series with NOT A = "OPEN" | A Ā | Complement |
|--|--|-----|---------------------|
| A+B = B+A | A in parallel with B = B in parallel with A | A | Commutative |
| A.B = B.A | A in series with B = B in series with A | _AB | Commutative |
| $\overline{A+B} = \overline{A}.\overline{B}$ | invert and replace OR with AND | | de Morgan's Theorem |
| $\overline{A.B} = \overline{A} + \overline{B}$ | invert and replace AND with OR | | de Morgan's Theorem |