

# MODULE DESCRIPTION FORM

## نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Integrated electronic circuits		Module Delivery
Module Type	C		<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	UOMU0207042		
ECTS Credits	7		
SWL (hr/sem)	175		
Module Level	UGII	Semester of Delivery	
Administering Department		College	NETC
Module Leader	Nasir Hussein Selman	e-mail	Coj.nas@atu.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	Ph.D.
Module Tutor	Name (if available)	e-mail	E-mail
Peer Reviewer Name	Nasir Hussein Selman	e-mail	Coj.nas@atu.edu.iq
Scientific Committee Approval Date	30/09/2025	Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	none	Semester	
Co-requisites module	none	Semester	

Module Aims, Learning Outcomes and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	<ol style="list-style-type: none"> <li>1. Understanding of Integrated Circuits.</li> <li>2. To Define types of Integrated Circuits.</li> <li>3. To Know the application on integrated Circuits.</li> <li>4. To understand the design of integrated Circuits.</li> </ol>
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	<ol style="list-style-type: none"> <li>1. Enabling student to know the concepts of IC</li> <li>2. learning the type of IC.</li> <li>3. Enabling student to know about the Construction of IC.</li> <li>4. Enabling student to design Many application of IC.</li> <li>5. Understand the basic operation of some types of IC</li> <li>6. Enabling student to Select the suitable IC for the applications</li> </ol>
<b>Indicative Contents</b> المحتويات الإرشادية	<ul style="list-style-type: none"> <li>• introduction to IC</li> <li>• Working with Power point</li> <li>• Theoretical lectures</li> <li>• Lab.</li> </ul>

Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
<b>Strategies</b>	<p>different examples.</p> <p>Using different styles of discussion that aim to connect the theoretical and practical sides.</p> <p>Asking questions and giving exercises that require analysis and conclusions related to lectures.</p> <p>Encourage students to participate in discussions and do the practical work.</p> <p>Encourage students to work in groups..</p>

Student Workload (SWL) الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	78	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	7
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	97	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	6
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	175		

Module Evaluation					
تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5 and 10	LO #1, #2 and #10, #11
	Assignments	2	10% (10)	2 and 12	LO #3, #4 and #6, #7
	Projects / Lab.	1	10% (10)	Continuous	All
	Report	1	10% (10)	13	LO #5, #8 and #10
Summative assessment	Midterm Exam	2hr	10% (10)	7	LO #1 - #7
	Final Exam	3hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
المنهاج الاسبوعي النظري	
	Material Covered
Week 1-2	Introduction to Integrated Circuits, Types of integrated circuits, Analog Integrated Circuits, digital integrated circuits, Programmable Integrated Circuits, Integrated Circuit Applications, Advantages of integrated circuits, Future trends in integrated circuit technology
Week 3-10	operational amplifier, applications (linear and non-linear applications): inverting, noninverting, voltage follower, adder, subtractor, integrator and differential, comparator, zero crossing detector, voltage bounding, log and antilog, Analog to Digital Converters, Digital to Analog Converters, active filters design (Butterworth filter).
Week 11-12	Feedback Amplifier, Oscillators: Positive feedback and oscillation, Stability of Oscillation, Oscillators using OP-AMP: Phase-shift Oscillator, Wien Bridge Oscillator, Hartley Oscillator, Crystal Oscillator.
Week 13-15	Timing circuits 555 timer applications, 555 timer as a mono stable multivibrator and a bistable multivibrator
Week 16	Final Exam

Delivery Plan (Weekly Lab. Syllabus)	
المنهاج الاسبوعي للمختبر	
	Material Covered
Week 1 & 2	Op-Amp Voltage Followers and Noninverting Amplifiers, Inverting Amplifiers
Week 3 & 4	Summing and Difference Amplifiers

<b>Week 5</b>	Differentiator and integrator
<b>Week 6 &amp; 7</b>	Lowpass, Bandpass and High pass active Filters
<b>Week 8</b>	Midterm exam
<b>Week 9 &amp; 10</b>	Analog to Digital Converters, Digital to Analog converters
<b>Week 11 &amp; 12</b>	Different Oscillators
<b>Week 13 &amp; 14</b>	555 IC Operating as Astable Multivibrator, 555 IC Operating as Bistable Multivibrator, 555 IC Operating as Monostable Multivibrator
<b>Week 15</b>	Final exam

<b>Learning and Teaching Resources</b> <b>مصادر التعلم والتدريس</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	"Electronic Devices and Circuit Theory", Robert Boylestad, Louis Nashelsky, 10th Edition , 2009.	Yes
<b>Recommended Texts</b>	electronic-devices-9th-edition-by-floyd	Yes
<b>Websites</b>		

<b>Grading Scheme</b> <b>مخطط الدرجات</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks %</b>	<b>Definition</b>
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.