

# MODULE DESCRIPTION FORM

## نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Mechanics (1)		Module Delivery	
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	UOMU031011			
ECTS Credits	7			
SWL (hr/sem)	175			
Module Level	1	Semester of Delivery		1
Administering Department	Type Dept. Code	College	Type College Code	
Module Leader	م.م ياسر مرتضى مجيد		e-mail	<a href="mailto:yasir.mortada.mjeed@uomus.edu.iq">yasir.mortada.mjeed@uomus.edu.iq</a>
Module Leader's Acad. Title		Module Leader's Qualification	Ph.D.	
Module Tutor	Name (if available)		e-mail	E-mail
Peer Reviewer Name	Name		e-mail	E-mail
Scientific Committee Approval Date		Version Number	1.0	

Relation with other Modules				
العلاقة مع المواد الدراسية الأخرى				
Prerequisite module	None		Semester	
Co-requisites module	None		Semester	

## Module Aims, Learning Outcomes and Indicative Contents

### أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<b>Module Aims</b> أهداف المادة الدراسية	1. A student may be able to speak English Language and listen to native speakers. 2. A student may be able to write and have creativity in his writing. 3. Being used to read different levels of paragraphs and pronounce correctly
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	Listening – Speaking – Reading- Writing
<b>Indicative Contents</b> المحتويات الإرشادية	<p>Indicative content includes the following.</p> <p><u>Part A - Circuit Theory</u></p> <p>DC circuits – Current and voltage definitions, Passive sign convention and circuit elements, Combining resistive elements in series and parallel. Kirchhoff's laws and Ohm's law. Anatomy of a circuit, Network reduction, Introduction to mesh and nodal analysis. [15 hrs]</p> <p>AC circuits I – Time dependent signals, average and RMS values. Capacitance and inductance, energy storage elements, simple AC steady-state sinusoidal analysis. [15 hrs]</p> <p>AC Circuits II - Phasor diagrams, definition of complex impedance, AC circuit analysis with complex numbers. [10 hrs]</p> <p>RL, RC and RLC circuits - Frequency response of RLC circuits, simple filter and band-pass circuits, resonance and Q-factor, use of Bode plots, use of differential equations and their solutions. Time response (natural and step responses). Introduction to second order circuits. [15 hrs]</p> <p>Revision problem classes [6 hrs]</p>

	<p><u>Part B - Analogue Electronics</u></p> <p>Fundamentals</p> <p>Resistive networks, voltage and current sources, Thevenin and Norton equivalent circuits, current and voltage division, input resistance, output resistance, coupling and decoupling capacitors, maximum power transfer, RMS and power dissipation, current limiting and over voltage protection. [15 hrs]</p> <p>Components and active devices – Components vs elements and circuit modeling, real and ideal elements. Introduction to sensors and actuators, self-generating vs modulating type sensors, simple circuit interfacing. [7 hrs]</p> <p>Diodes and Diode circuits – Diode characteristics and equations, ideal vs real. Signal conditioning, clamping and clipping, rectification and peak detection, photodiodes, LEDs, Zener diodes, voltage stabilization, voltage reference, power supplies. [15 hrs]</p>
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<b>Learning and Teaching Strategies</b> استراتيجيات التعلم والتعليم	
<b>Strategies</b>	<p>Type something like: The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.</p>

<b>Student Workload (SWL)</b> الحمل الدراسي للطالب			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	86	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعياً	4

<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطلاب خلال الفصل	64	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطلاب أسبوعيا	2
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطلاب خلال الفصل	150		

Module Evaluation تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	10% (10)	5, 10	LO #1, 2, 10 and 11
	<b>Assignments</b>	2	10% (10)	2, 12	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	1	10% (10)	Continuous	
	<b>Report</b>	1	10% (10)	13	LO # 5, 8 and 10
<b>Summative assessment</b>	<b>Midterm Exam</b>	2 hr	10% (10)	7	LO # 1-7
	<b>Final Exam</b>	2hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
	Material Covered
<b>Week 1</b>	Unit 1-HELLO
<b>Week 2</b>	Unit 1-HELLO
<b>Week 3</b>	Unit 3- ALL ABOUT YOU
<b>Week 4</b>	Unit 3- ALL ABOUT YOU
<b>Week 5</b>	Unit 4- FAMILY AND FRIEND
<b>Week 6</b>	Unit 5- THE WAY I LIVE Unit 6-EVERY DAY
<b>Week 7</b>	Unit 5- THE WAY I LIVE Unit 6-EVERY DAY
<b>Week 8</b>	Unit 7- MY FAVOURITE Unit 8- WHERE I LIVE
<b>Week 9</b>	Unit 7- MY FAVOURITE Unit 8- WHERE I LIVE
<b>Week 10</b>	Review

<b>Week 11</b>	Unit 9- TIMES PAST
<b>Week 12</b>	Unit 9- TIMES PAST
<b>Week 13</b>	Unit 9- TIMES PAST
<b>Week 14</b>	14 --- Unit 10- WE HAD A GREAT TIME
<b>Week 15</b>	Unit 9- Relationships
<b>Week 16</b>	Unit 10- Obsessions
<b>Week 17</b>	Unit 11- Tell me about it!

<b>Delivery Plan (Weekly Lab. Syllabus)</b> المنهاج الاسبوعي للمختبر	
	<b>Material Covered</b>
<b>Week 1</b>	Unit 9- TIMES PAST
<b>Week 2</b>	Unit 9- TIMES PAST
<b>Week 3</b>	Unit 9- TIMES PAST
<b>Week 4</b>	14 --- Unit 10- WE HAD A GREAT TIME
<b>Week 5</b>	Unit 9- Relationships
<b>Week 6</b>	Unit 10- Obsessions
<b>Week 7</b>	Unit 11- Tell me about it!

<b>Learning and Teaching Resources</b> مصادر التعلم والتدريس		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	Fundamentals of Electric Circuits, C.K. Alexander and M.N.O Sadiku, McGraw-Hill Education	Yes
<b>Recommended Texts</b>	DC Electrical Circuit Analysis: A Practical Approach Copyright Year: 2020, dissidents.	No
<b>Websites</b>	<a href="https://www.coursera.org/browse/physical-science-and-engineering/electrical-engineering">https://www.coursera.org/browse/physical-science-and-engineering/electrical-engineering</a>	

## Grading Scheme

### مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> – Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> – Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> – Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> – Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.