

MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Mechanics (1)		Module Delivery
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	UOMU031011		
ECTS Credits	7		
SWL (hr/sem)	175		
Module Level	1	Semester of Delivery	1
Administering Department	Type Dept. Code	College	Type College Code
Module Leader	م. ياسر مرتضى مجید	e-mail	yasir.mortada.mjeed@uomus.edu.iq
Module Leader's Acad. Title		Module Leader's Qualification	Ph.D.
Module Tutor	Name (if available)	e-mail	E-mail
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date		Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None		Semester
Co-requisites module	None		Semester

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Aims أهداف المادة الدراسية	1. A student may be able to speak English Language and listen to native speakers. 2. A student may be able to write and have creativity in his writing. 3. Being used to read different levels of paragraphs and pronounce correctly
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	Listening – Speaking – Reading- Writing
Indicative Contents المحتويات الإرشادية	<p>Indicative content includes the following.</p> <p><u>Part A - Circuit Theory</u></p> <p>DC circuits – Current and voltage definitions, Passive sign convention and circuit elements, Combining resistive elements in series and parallel. Kirchhoff's laws and Ohm's law. Anatomy of a circuit, Network reduction, Introduction to mesh and nodal analysis. [15 hrs]</p> <p>AC circuits I – Time dependent signals, average and RMS values. Capacitance and inductance, energy storage elements, simple AC steady-state sinusoidal analysis. [15 hrs]</p> <p>AC Circuits II - Phasor diagrams, definition of complex impedance, AC circuit analysis with complex numbers. [10 hrs]</p> <p>RL, RC and RLC circuits - Frequency response of RLC circuits, simple filter and band-pass circuits, resonance and Q-factor, use of Bode plots, use of differential equations and their solutions. Time response (natural and step responses). Introduction to second order circuits. [15 hrs]</p> <p>Revision problem classes [6 hrs]</p>

	<p><u>Part B - Analogue Electronics</u></p> <p>Fundamentals Resistive networks, voltage and current sources, Thevenin and Norton equivalent circuits, current and voltage division, input resistance, output resistance, coupling and decoupling capacitors, maximum power transfer, RMS and power dissipation, current limiting and over voltage protection. [15 hrs]</p> <p>Components and active devices – Components vs elements and circuit modeling, real and ideal elements. Introduction to sensors and actuators, self-generating vs modulating type sensors, simple circuit interfacing. [7 hrs]</p> <p>Diodes and Diode circuits – Diode characteristics and equations, ideal vs real. Signal conditioning, clamping and clipping, rectification and peak detection, photodiodes, LEDs, Zener diodes, voltage stabilization, voltage reference, power supplies. [15 hrs]</p>
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<h3>Learning and Teaching Strategies</h3> <p>استراتيجيات التعلم والتعليم</p>	
Strategies	Type something like: The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.

<h3>Student Workload (SWL)</h3> <p>الحمل الدراسي للطالب</p>			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	86	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4

Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	64	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	2
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation					
تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	2	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	1	10% (10)	Continuous	
	Report	1	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	2hr	50% (50)	16	All
Total assessment		100% (100 Marks)			

Delivery Plan (Weekly Syllabus)	
المنهاج الأسبوعي النظري	
	Material Covered
Week 1	Unit 1-HELLO
Week 2	Unit 1-HELLO
Week 3	Unit 3- ALL ABOUT YOU
Week 4	Unit 3- ALL ABOUT YOU
Week 5	Unit 4- FAMILY AND FRIEND
Week 6	Unit 5- THE WAY I LIVE Unit 6-EVERY DAY
Week 7	Unit 5- THE WAY I LIVE Unit 6-EVERY DAY
Week 8	Unit 7- MY FAVOURITE Unit 8- WHERE I LIVE
Week 9	Unit 7- MY FAVOURITE Unit 8- WHERE I LIVE
Week 10	Review

Week 11	Unit 9- TIMES PAST
Week 12	Unit 9- TIMES PAST
Week 13	Unit 9- TIMES PAST
Week 14	14 --- Unit 10- WE HAD A GREAT TIME
Week 15	Unit 9- Relationships
Week 16	Unit 10- Obsessions
Week 17	Unit 11- Tell me about it!

Delivery Plan (Weekly Lab. Syllabus)	
المنهاج الأسبوعي للمختبر	
	Material Covered
Week 1	Unit 9- TIMES PAST
Week 2	Unit 9- TIMES PAST
Week 3	Unit 9- TIMES PAST
Week 4	14 --- Unit 10- WE HAD A GREAT TIME
Week 5	Unit 9- Relationships
Week 6	Unit 10- Obsessions
Week 7	Unit 11- Tell me about it!

Learning and Teaching Resources		
مصادر التعلم والتدریس		
	Text	Available in the Library?
Required Texts	Fundamentals of Electric Circuits, C.K. Alexander and M.N.O Sadiku, McGraw-Hill Education	Yes
Recommended Texts	DC Electrical Circuit Analysis: A Practical Approach Copyright Year: 2020, dissidents.	No
Websites	https://www.coursera.org/browse/physical-science-and-engineering/electrical-engineering	

Grading Scheme

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D – Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note: Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.