



MODULE DESCRIPTOR FORM

نموذج وصف المادة الدراسية

Module Information

معلومات المادة الدراسية

Module Title	MICROPROCESSOR	Module Delivery	
Module Type	CORE	✓ Theory Lecture ✓ Lab Tutorial ✓ Practical ✓ Seminar	
Module Code	UOM00205055		
ECTS Credits	5		
SWL (hr/sem)	125		
Module Level	3	Semester of Delivery	1
Administering Department	DEPARTMENT OF ELECTRICAL ENGINEERING TECHNIQUES	College	AL-MUSTAQBAL UNIVERSITY/ENGINEERING TECHNICAL COLLEGE
Module Leader	Mohammed Fadhil Mohammed	e-mail	Mohammed.fadhil1@uomus.edu.iq
Module Leader's Acad. Title		Module Leader's Qualification	Doctor
Module Tutor	None	e-mail	
Peer Reviewer Name	Asmaa dheyaa saed	e-mail	asmaa.dheyaa@uomus.edu.iq
Review Committee Approval	1/09/2025	Version Number	1.0

Relation With Other Modules

العلاقة مع المواد الدراسية الأخرى

Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Objectives أهداف المادة الدراسية	The course objective is to introduce the operation, programming, and application of microprocessor
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	<ol style="list-style-type: none">1- Analyze the functional block of the 8086 microprocessors.2- Write an assembly language program for the given problem.3- Use instructions for different addressing modes.4- Develop an assembly language program using assembler.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <ul style="list-style-type: none">• <u>Part A</u> – Introduction to Microcomputers, Introduction to Microcomputers; Microprocessor vs. Microcontrollers. Introduction to Microprocessor-Based System Design. Microprocessor Memories, Memory Operation

	<p>Microprocessor System, Need for memory segmentation, Microprocessor Architecture, Bus Interfacing Unit (BIU), Execution Unit (EU). Addressing Modes, Assembly language, Mnemonics, Assembler program Different Instruction Types, Data Transfer, Shift Instruction, Examples The Programmable Peripheral Interface (PPI). [20 hrs]</p> <ul style="list-style-type: none"> • <u>Part B Programmable logic controller (PLC).</u> Programmable logic controller (PLC), Advantages of PLC, Architecture of PLC, Mechanical design of PLC How does a PLC operate? What are input/output devices Programming Language for PLC, Ladder Logic, Functional block diagram (FBD), Instruction list Examples on Programming PLC [10 hrs]
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Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	<p>1-Hands-on Experiments: Engage students in practical experiments to deepen their understanding of the operation, programming, and application of microprocessor.</p> <p>2-Simulation Software: Simulation Software: Use proteus software for virtual circuit design and programming based on a microprocessor.</p> <p>3-Problem-solving Exercises: Include various problem-solving exercises for programming and the application of microprocessor techniques.</p> <p>4-Group Projects: Assign collaborative projects for programming, and application of microprocessors.</p> <p>5-Real-world Applications: Discuss practical applications of microprocessors in different devices and systems.</p> <p>5-Interactive Discussions: Encourage student participation and critical thinking through open-ended questions.</p> <p>6-Conceptual Understanding: Focus on intuitive understanding alongside mathematical analysis.</p> <p>7-Assessment Variety: Use diverse assessment methods to gauge student understanding.</p> <p>8-Office Hours and Support: Offer individualized assistance through office hours or online support.</p>
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Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	63	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	62	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

	Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
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Formative assessment	Quizzes	4	10% (10)	2, 12	LO # 3, and 4
	Assignments	8	10% (10)	2, 12	LO # 3, and 4
	Projects / Lab.	7	10% (10)	2, 12	LO # 2, and 4
	Report	8	10% (10)	2, 12	All
Summative assessment	Midterm Exam	2 hr	10% (10)	8	LO # 1-4
	Final Exam	3 hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

	Material Covered
1,2	• Introduction to Microcomputers; Microprocessor vs. Microcontrollers. Introduction to Microprocessor-Based System Design.
3,4	Microprocessor Memories, Memory Operation, Read Only Memories (ROM), RAM Architecture, Dynamic RAM structure and operation
5	Microprocessor System, Need for memory segmentation, Microprocessor Architecture, Bus Interfacing Unit (BIU), Execution Unit (EU), Segment Group:, DATA Group Registers
6,7	Addressing Modes, Assembly language, Mnemonics, Assembler program,
8,9	Different Instruction Types, Data Transfer, Shift Instruction, Examples
10	The Programmable Peripheral Interface (PPI)
11	Programable logic controller (PLC), Advantages of PLC, Architecture of PLC, Mechanical design of PLC
12	How does a PLC operate? What are input/output devices?
13,14	Programming Language for PLC , Ladder Logic, Functional block diagram (FBD), Instruction list, Examples on Programming PLC
15	• Final Examination

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	Material Covered
1	Lab 1: Identify various pins of the given Microprocessor.
2	Lab 2: Use Assembly Language Programming Tools and functions
3	Lab 3: Use different addressing mode instruction in program Write an Assembly Language Program (ALP) to add two given 8 and 16 bits numbers Write an Assembly Language Program (ALP) to subtract two given 8 and 16 bits numbers
4	Lab 4: (a) Write an Assembly Language Program (ALP) to multiply two given 8 and 16 bits unsigned numbers (b)Write an Assembly Language Program (ALP) to multiply two given 8 and 16 bits signed numbers
5	Lab 5: (a) Write an Assembly Language Program (ALP) to divide two given 8 and 16 bits unsigned numbers (b)Write an Assembly Language Program (ALP) to divide two given 8 and 16 bits signed numbers
6	Lab 6: Write an Assembly Language Program (ALP) to add, subtract, multiply, and divide two BCD numbers.
7	Lab 7: Implement loop in assembly language program

	Write an Assembly Language Program (ALP) to sum of series of Hexadecimal numbers. (b) Write an Assembly Language Program (ALP) to sum of series of BCD numbers.
8	Lab 8: (a) Write an Assembly Language Program (ALP) to find the smallest number from an array of n numbers. (b) Write an Assembly Language Program (ALP) to find the largest number from an array of n numbers
9	Lab 9: (a) Write an Assembly Language Program (ALP) to arrange numbers in an array in ascending order. (b) Write an Assembly Language Program (ALP) to arrange numbers in an array in descending order.
10	Lab 10: (a) Write an Assembly Language Program (ALP) to arrange elements string in reverse order. (b) Write an Assembly Language Program (ALP) to find string length.
11	Lab 11: (a) Write an Assembly Language Program (ALP) to check whether a given number is an ODD or EVEN. (b) Write an Assembly Language Program (ALP) to count ODD and/or EVEN numbers in an array.
12	Lab 12: (a) Write an Assembly Language Program (ALP) to check whether a given number is a POSITIVE or NEGATIVE. (b) Write an Assembly Language Program (ALP) to count POSITIVE and/or NEGATIVE numbers in an array
13	Lab 13: (a) Write an Assembly Language Program (ALP) to count the number of '1' in a given number. (b) Write an Assembly Language Program (ALP) to count the number of '0' in a given number.
14	Lab 14: Review

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Hall, Douglas V. MICROPROCESSORS AND INTERFACING. 1992.	Yes
Recommended Texts	Walter A. Triebel, Avtar Singh. The 8088 and 8086 Microprocessors: Programming, Interfacing, Software, Hardware, and Applications (4th Edition) - Instructor's Solution Manual [4th ed.] 0130930814, 9780130930811. Prentice Hall 2002	No

Websites	Assembly_Programming
	https://www.tutorialspoint.com/assembly_programming/index.htm

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.